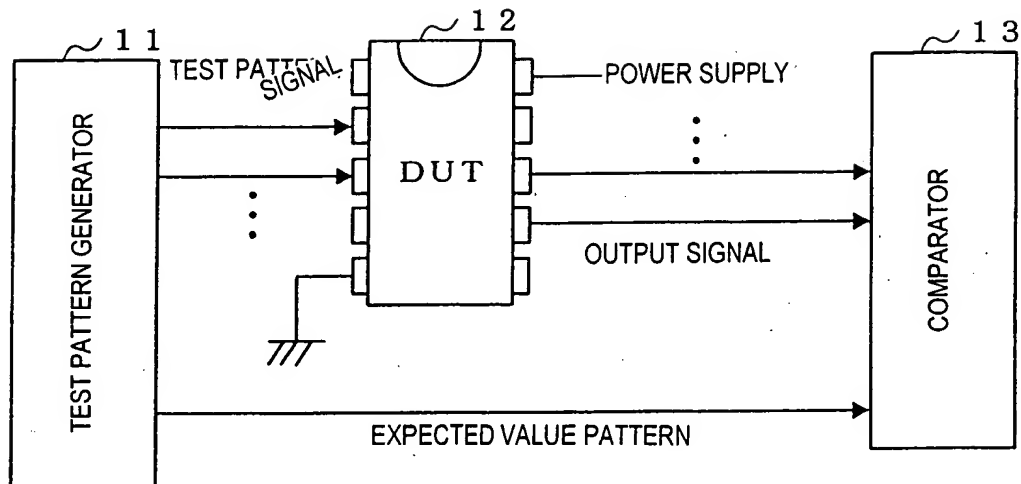


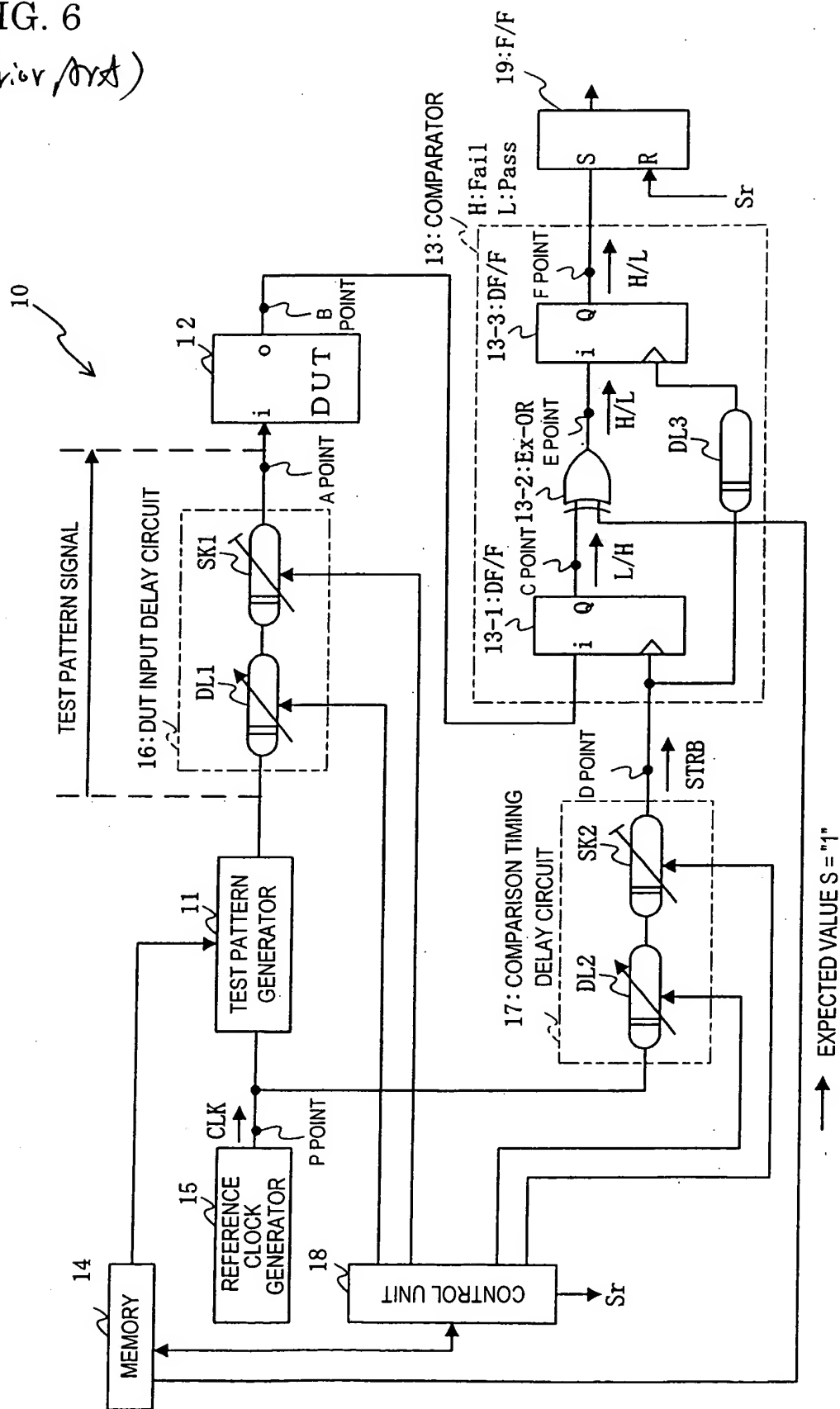
FIG. 5

(Prior Art)



1 0 : SEMICONDUCTOR TEST APPARATUS

FIG. 6
(Prior art)



mark-up

FIG. 7
(Prior Art)

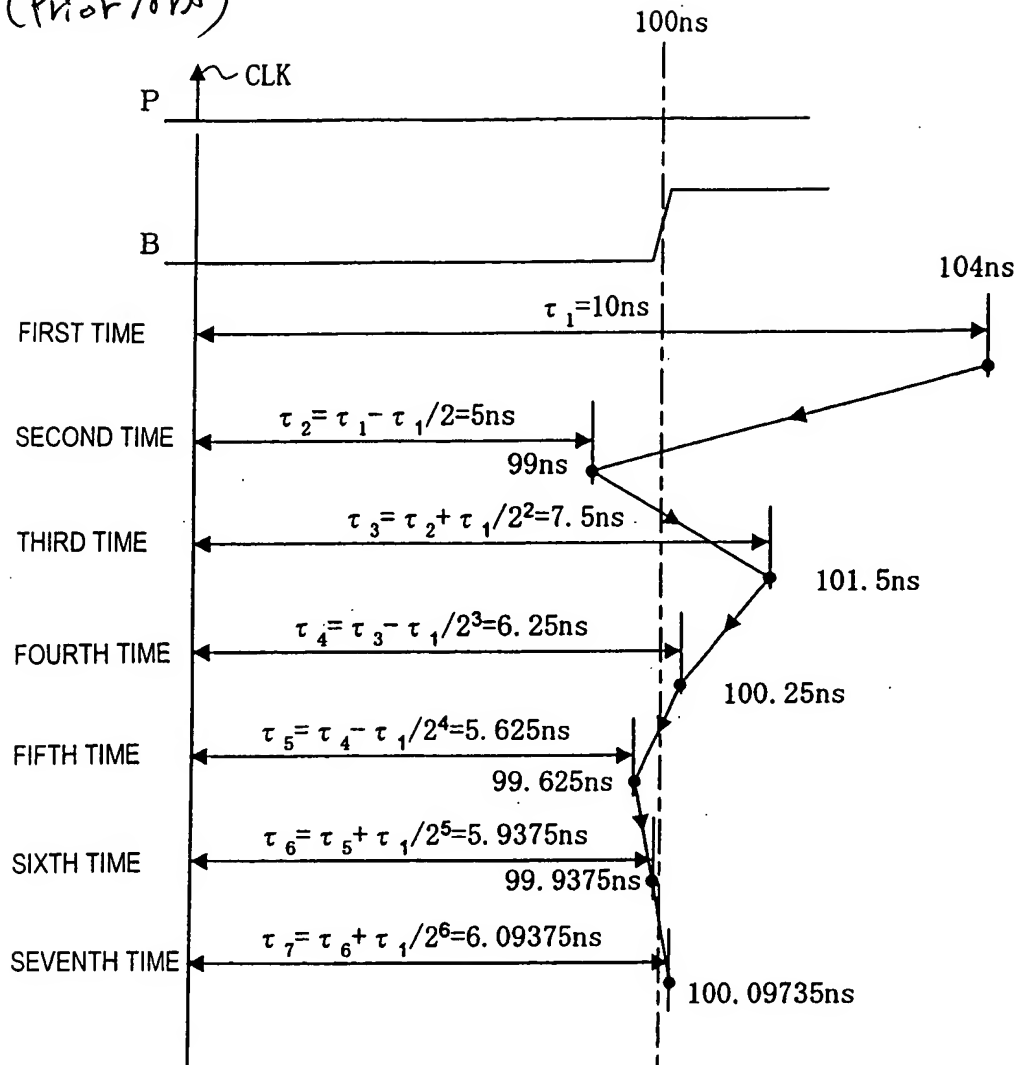


FIG. 8
(Prior Art)

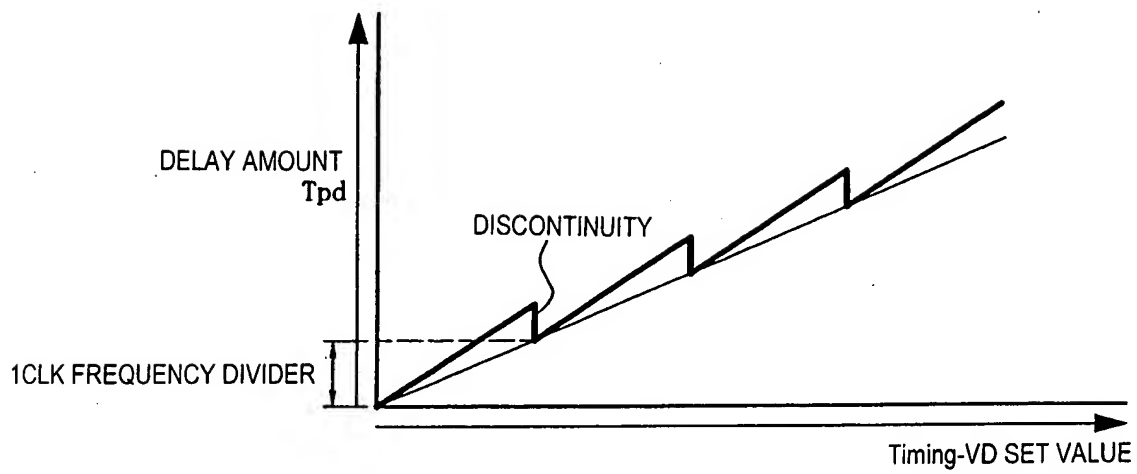
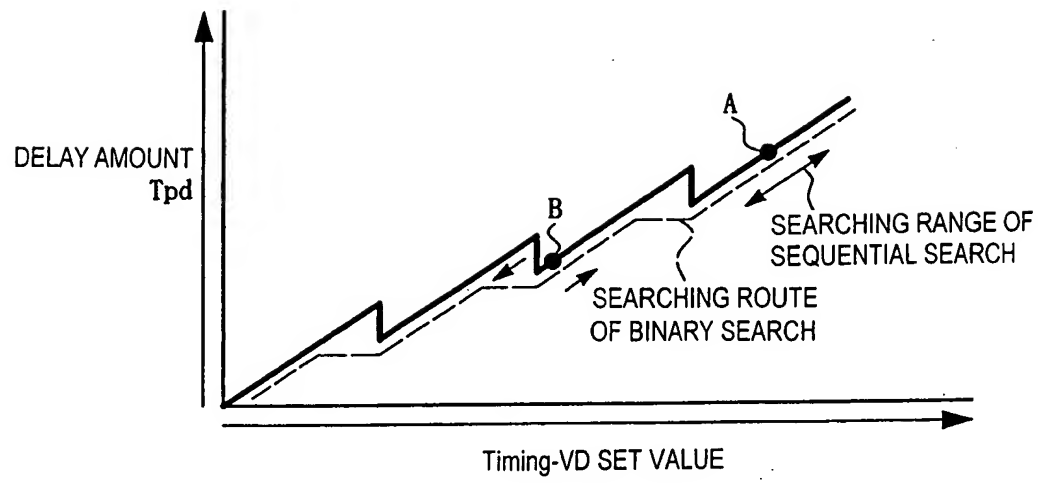


FIG. 9
(Prior Art)



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FIG. 10
(Prior Art)

